

Amendments to the Claims:

This listing of claims replaces all prior versions and listings of claims in the application:

Listing of Claims:

1. (Currently amended) A junction field-effect transistor (20) comprising:
 - a first conductivity type semiconductor layer (1) having a channel region;
 - a buffer layer (3) formed on said channel region; and
 - a second conductivity type doped region (4a, 4b) formed on said buffer layer (3), wherein a first conductivity type carrier concentration in said buffer layer (3) is lower than a first conductivity type carrier concentration in said first conductivity type semiconductor layer (1).
2. (Currently amended) The junction field effect transistor (20) according to claim 1, wherein said first conductivity type carrier concentration in said buffer layer (3) is not more than one tenth of said first conductivity type carrier concentration in said first conductivity type semiconductor layer (1).
3. (Currently amended) The junction field-effect transistor (20) according to claim 1, wherein said first conductivity type semiconductor layer (1) is composed of silicon carbide.
4. (Currently amended) The junction field-effect transistor (20) according to claim 1, further comprising a second conductivity type semiconductor layer (5a, 5b) formed under said channel region.
5. (Currently amended) The junction field-effect transistor (20a) according to claim 4, wherein said second conductivity type semiconductor layer (5a, 5b) is formed by implanting dopant ions,

the junction field-effect transistor further comprises another buffer layer (18) formed under said channel region on said second conductivity type semiconductor layer,

a first conductivity type carrier concentration in said another buffer layer (18) is lower than the first conductivity type carrier concentration in said first conductivity type semiconductor layer (1).

6. (Currently amended) The junction field-effect transistor (20a) according to claim 5, wherein said first conductivity type carrier concentration in said another buffer layer (18) is not more than one tenth of said first conductivity type carrier concentration in said first conductivity type semiconductor layer (1).

7. (Currently amended) The junction field-effect transistor (20) according to claim 1, further comprising a semiconductor substrate (6) composed of n-type silicon carbide, wherein said first conductivity type semiconductor layer (1) is formed on one main surface of said semiconductor substrate (6).

8. (Currently amended) The junction field-effect transistor (21) according to claim 7, further comprising:

a gate electrode (11a, 11b) formed on the surface of said second conductivity type doped region (4a, 4b),

an electrode (13a, 13b), either a source electrode or a drain electrode, formed on the surface of said first conductivity type semiconductor layer (1), and

another electrode (15), either a drain electrode or a source electrode, formed on another main surface of said semiconductor substrate (6).

9. (Currently amended) The junction field-effect transistor (21) according to claim 7, further comprising:

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a gate electrode ~~(11)~~ formed on the surface of said second conductivity type doped region ~~(4a)~~, and

a source electrode ~~(13)~~ and a drain electrode ~~(15)~~ formed on the surface of said first conductivity type semiconductor layer ~~(1)~~.